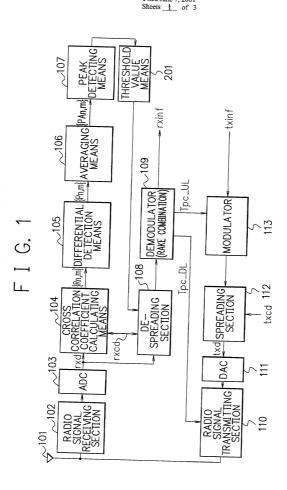
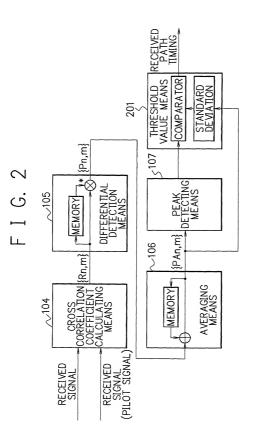
## Toshifumi SATO "Received Path Timing Detecting Circuit at DS-CDMA System" Q64872 Filed June 7, 2001



Toshifumi SATO
"Received Path Timing Detecting Circuit at DS-CDMA System"
Q64872
Filed June 7, 2001
Sheets \_2\_ of 3



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DEDICATED PHYSICAL		-
DATA CHANNEL	DATA SIGNAL (1 FRAME)	Т
DEDICATED PHYSICAL CONTROL CHANNEL (DPCCH)	SLOT 1 SLOT 2 SLOT 3 SLOT 15	2 2
	FRAME (10ms)	<u></u>